

Wednesday 5<sup>th</sup> and Thursday 6<sup>th</sup> of February 2025,  
at Radisson Blu Royal Garden Hotel in Trondheim, Norway  
(Tutorials/Workshops on Tuesday 4<sup>th</sup>)

## Call for Presentations and Exhibition

FPGA-forum is a unique 2-day seminar and exhibition. At this yearly event, a large number of senior designers and decision-makers in the Norwegian embedded industry gather to share their experience, learn about new technology and methodology, socialise and build their professional network.

The FPGA-forum committee is now preparing FPGA-forum 2025 and invites industry, academia and vendors to submit proposals for good technical presentations for the FPGA community. You are also invited to participate in the exhibition. The exhibition and presentations are co-located and will run for two full days. (See [www.fpga-forum.no](http://www.fpga-forum.no))

### Presentations

Each presentation will be given a default time slot of 30 minutes - including time for questions. There may also be some 60-minute time slots available. FPGA-forum has a mix of user, industrial, academic and vendor presentations - and a very strong focus on being a high-quality seminar with interesting technical presentations for experienced FPGA designers. Presentations from vendors are thus expected to have good and interesting technical content with very little direct marketing.

Please submit your presentation proposal(s) before the deadline given under [fpga-forum.no/call-for-contributions](http://fpga-forum.no/call-for-contributions) by email to [contact@fpga-forum.no](mailto:contact@fpga-forum.no). Please note that this deadline may be dynamically extended. Remember to include presentation title and abstract, name of presenter and company. Feel free to submit multiple prioritised proposals, and please let us know if you would be interested in a 60-minute time slot or perhaps two 30-minute time slots.

Note that presenters pay a reduced registration fee - unless they are covered by the exhibition fee. (See [www.fpga-forum.no](http://www.fpga-forum.no) for more info)

### Colleagues from abroad are invited to participate

There will be two tracks on both days. Presentations will be in English and Norwegian. On average, around 80% are in English (some on request).

### Exhibition

Please see [www.fpga-forum.no/exhibitions/](http://www.fpga-forum.no/exhibitions/) for exhibition fees and more info.

### Accommodation and Travel

See <http://www.fpga-forum.no/travel-venue/> for accommodation and travel. Please note you have to contact the hotel yourself.

Best regards from

The FPGA-forum committee

- Arild Kjerstad, Kongsberg
- Heidi Skaar Johannessen, Norxe
- Jan Anders Mathisen, Silica/Xilinx
- Jim Tørresen, University of Oslo
- Per Gunnar Kjeldsberg, NTNU
- Espen Tallaksen, EmLogic

Questions may be addressed to: [contact@fpga-forum.no](mailto:contact@fpga-forum.no)